

FORMATION OF UNDERLYING FILM / AMORPHOUS SEMICONDUCTOR FILM

Fig. 1(A)

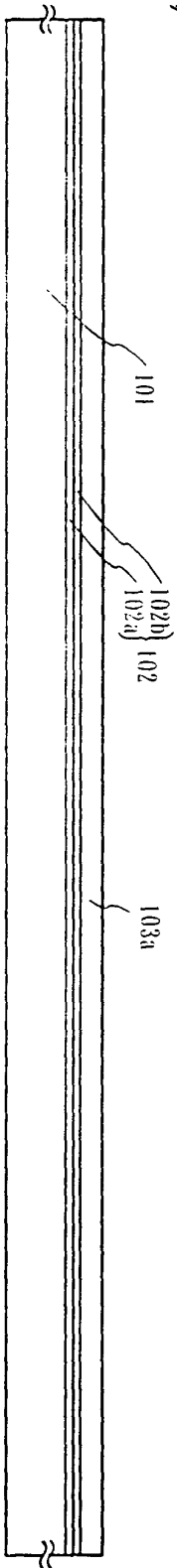


Fig. 1(B) CRYSTALLIZATION STEP

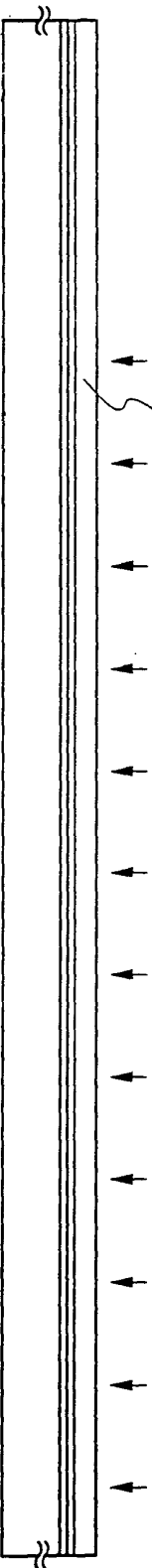


Fig. 1(C) FORMATION OF ISLAND SHAPE SEMICONDUCTOR LAYER / GATE INSULATION FILM

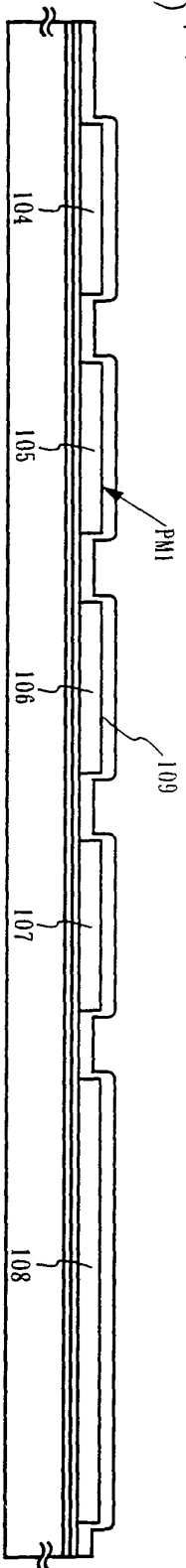


Fig. 1(D) FORMATION OF HEAT-RESISTANT CONDUCTOR LAYER

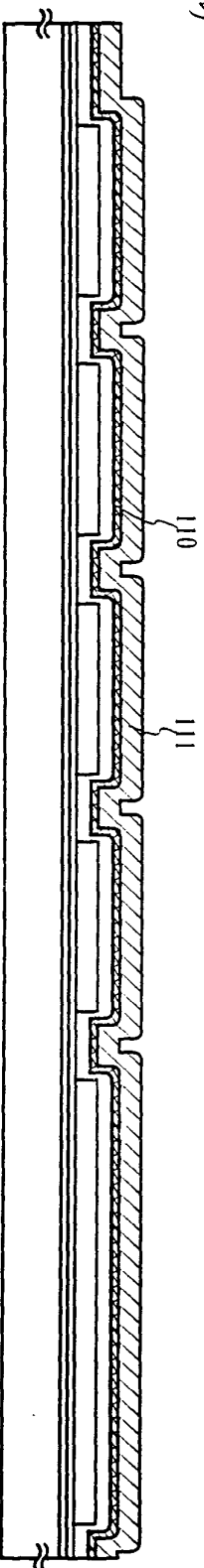
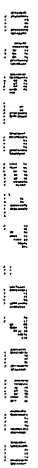
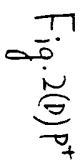
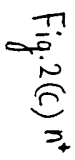


Fig. 2(A)



FORMATION OF RESIN INTER-LAYER INSULATION FILM / PERFORATION OF CONTACT HOLE /
FORMATION OF LEAD WIRE AND PIXEL ELECTRODE

FIRST P CHANNEL TFT 200

FIRST n CHANNEL TFT 201

SECOND P CHANNEL TFT 202

SECOND n CHANNEL TFT 203

PIXEL TFT 204

HOLDING CAPACITANCE 205

LOGIC CIRCUIT

DRIVING CIRCUIT

SAMPLING CIRCUIT

PIXEL UNIT

147 148 153 154 149 150 155 156 PM5 151 152 250 158 227 251 157

207b 207a 206 208a 208b 211 210 209 212 214b 214a 213 215a 215b 219 217 216 218 220 226 223 221 224 222 225 228 229

A A

Fig. 4(A) n^- DOPING STEP \downarrow

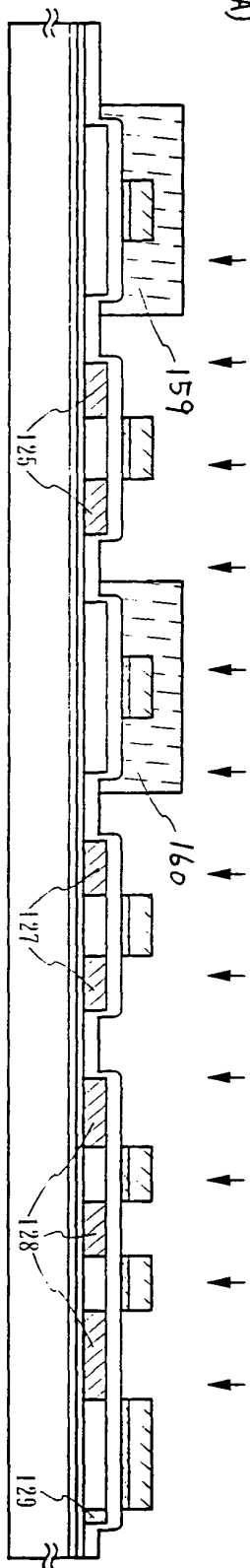


Fig. 4(B) n^+ DOPING STEP

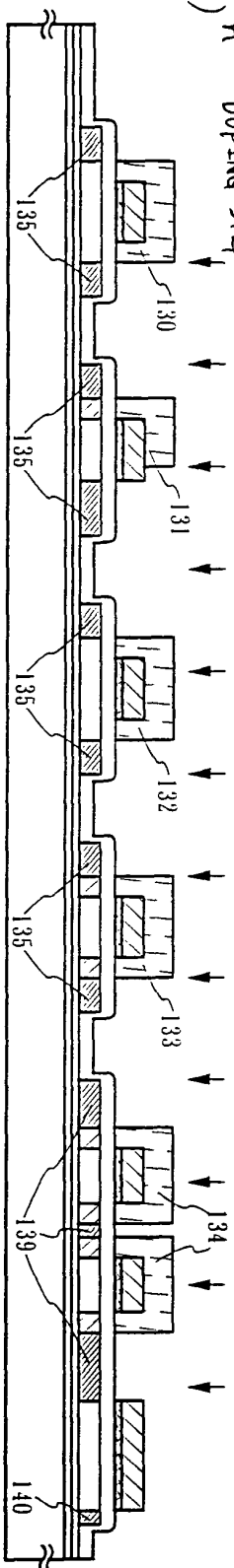


Fig. 4(c) P^+ DOPING STEP

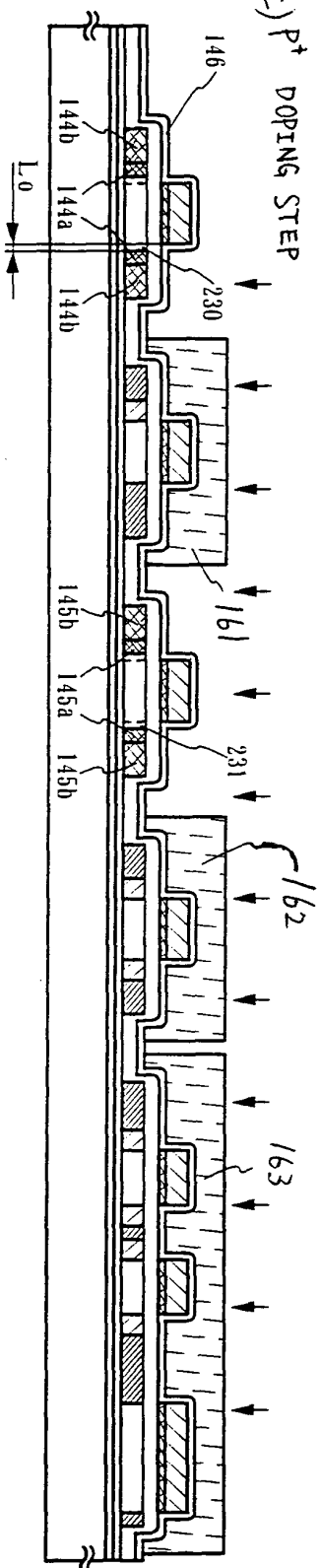


Fig. 5(A)

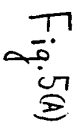


Fig. 5(B)

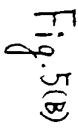
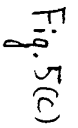
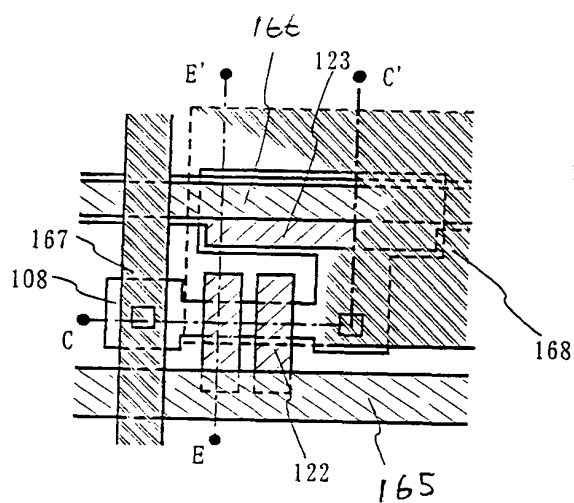
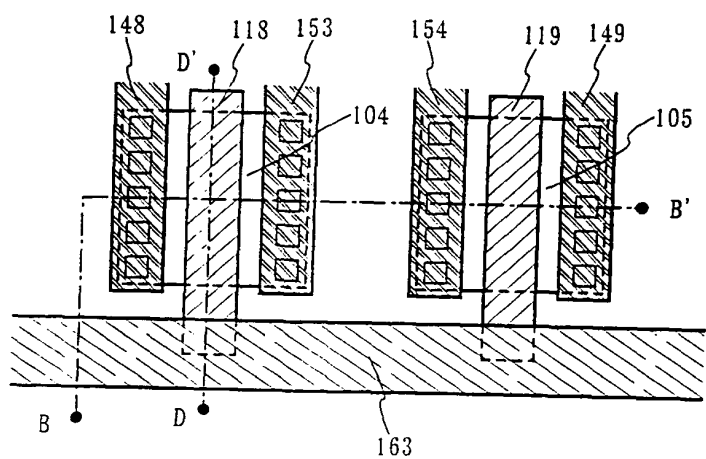


Fig. 5(c)





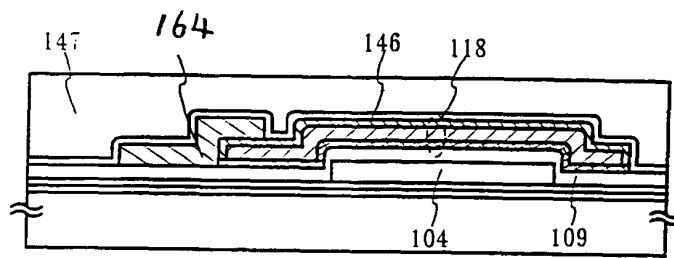


Fig. 7(A)

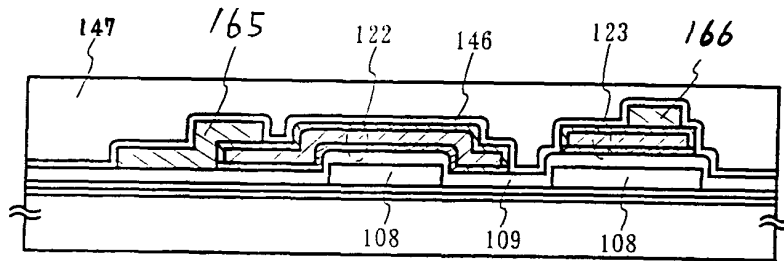


Fig. 7(B)

Fig. 8(A)

Fig. 8(B)

Fig. 8(c)

[illegible]

Fig. 10(A)

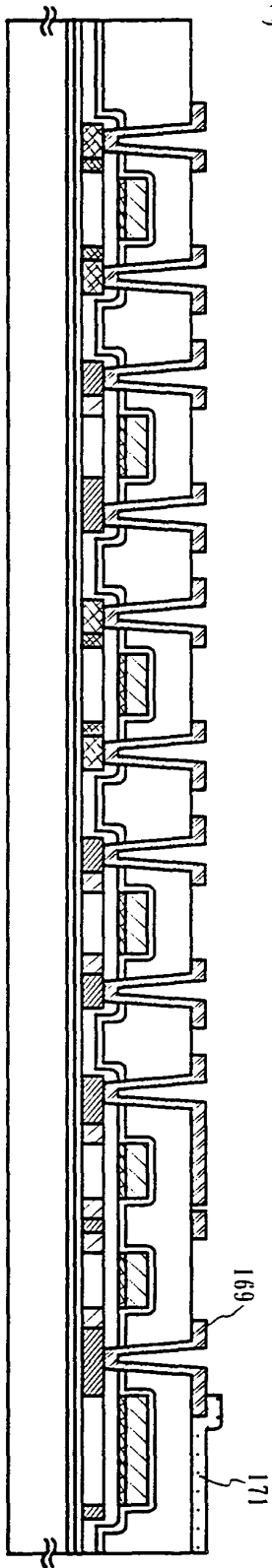
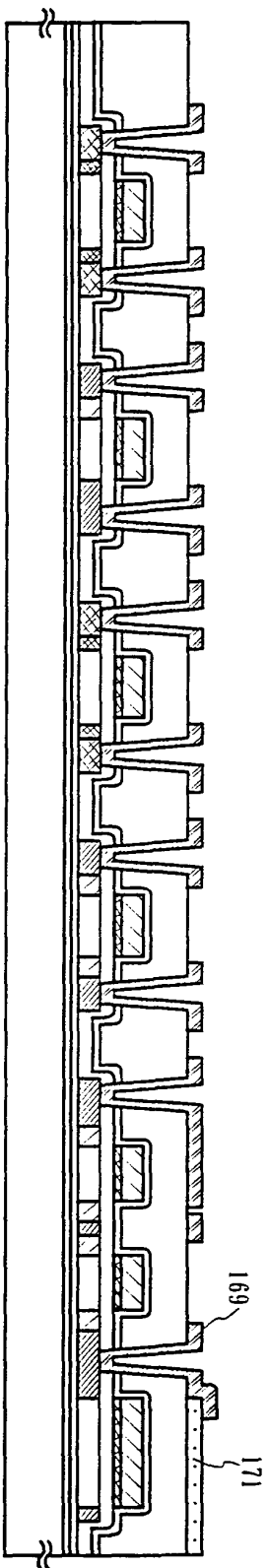


Fig. 10(B)



FORMATION OF ORIENTATION FILM / FORMATION OF SPACER /
 Fig. 11(A) RUBBING TREATMENT

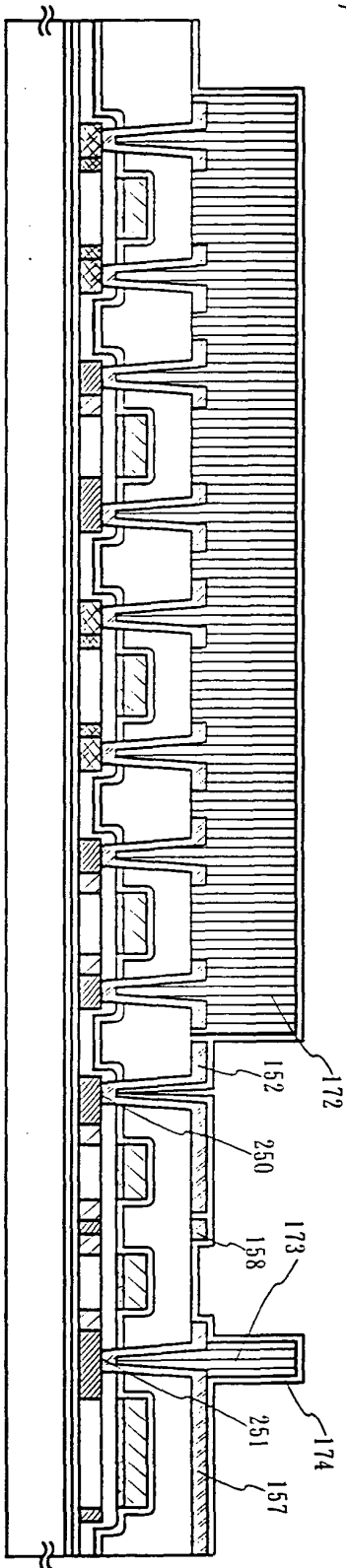


Fig. 11(B) BONDING OF OPPOSED SUBSTRATE

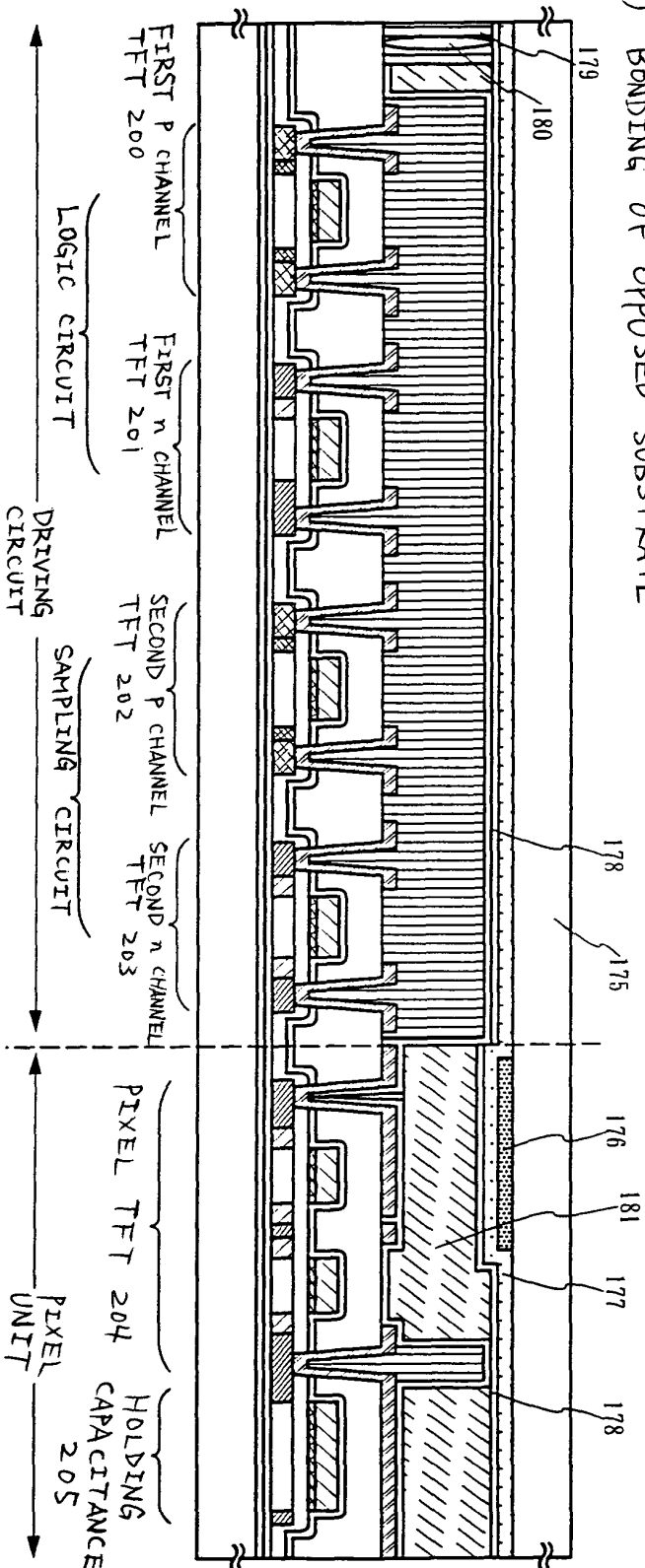


Fig. 12

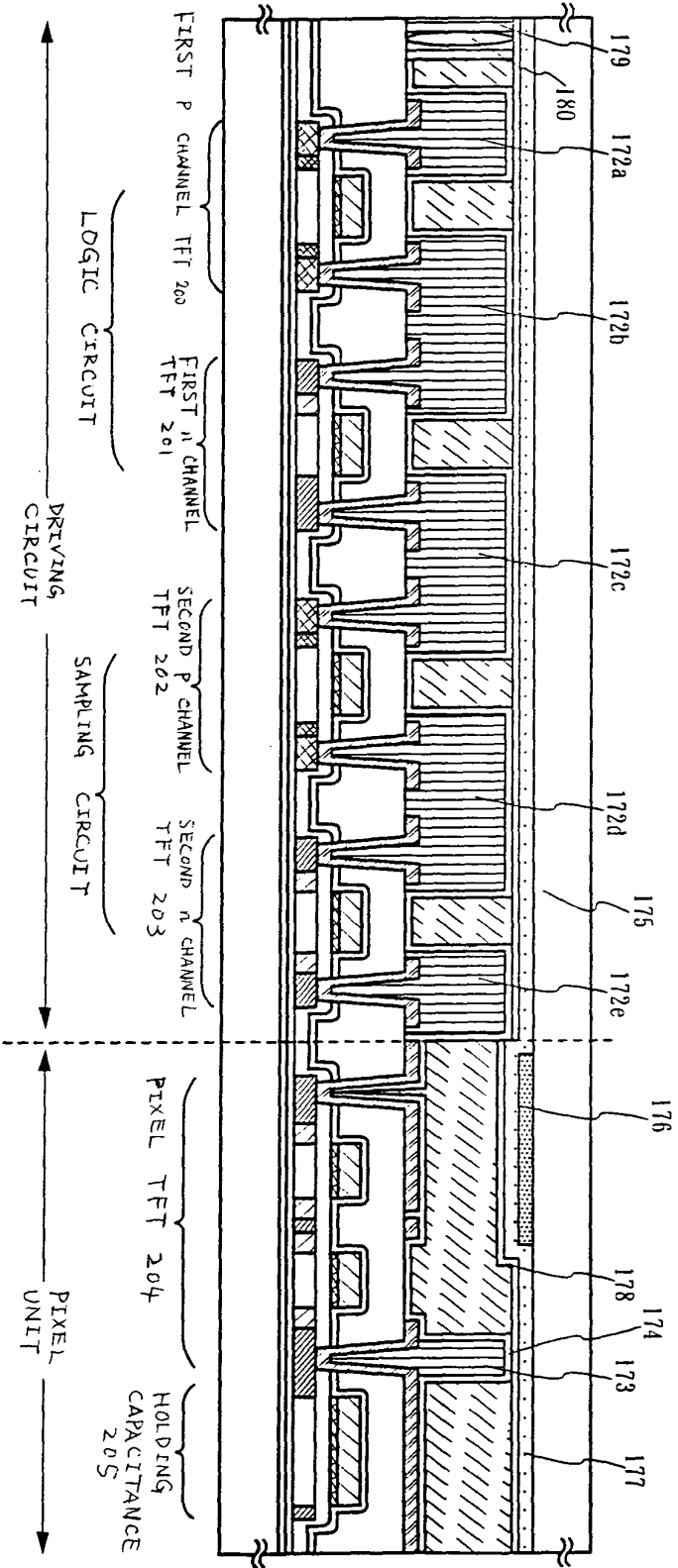
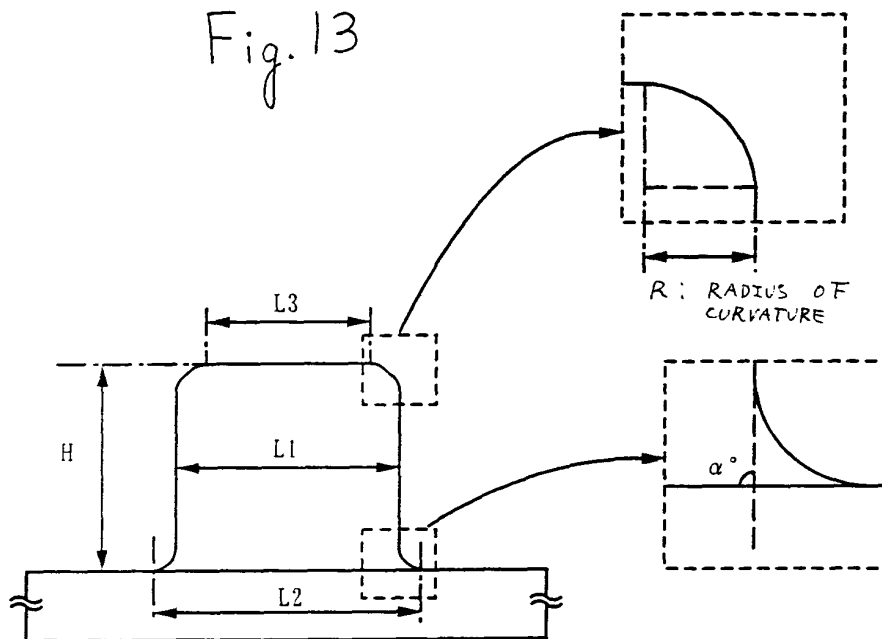


Fig. 13



SECTIONAL TYPICAL VIEW
OF SPACER

005040-2-201500

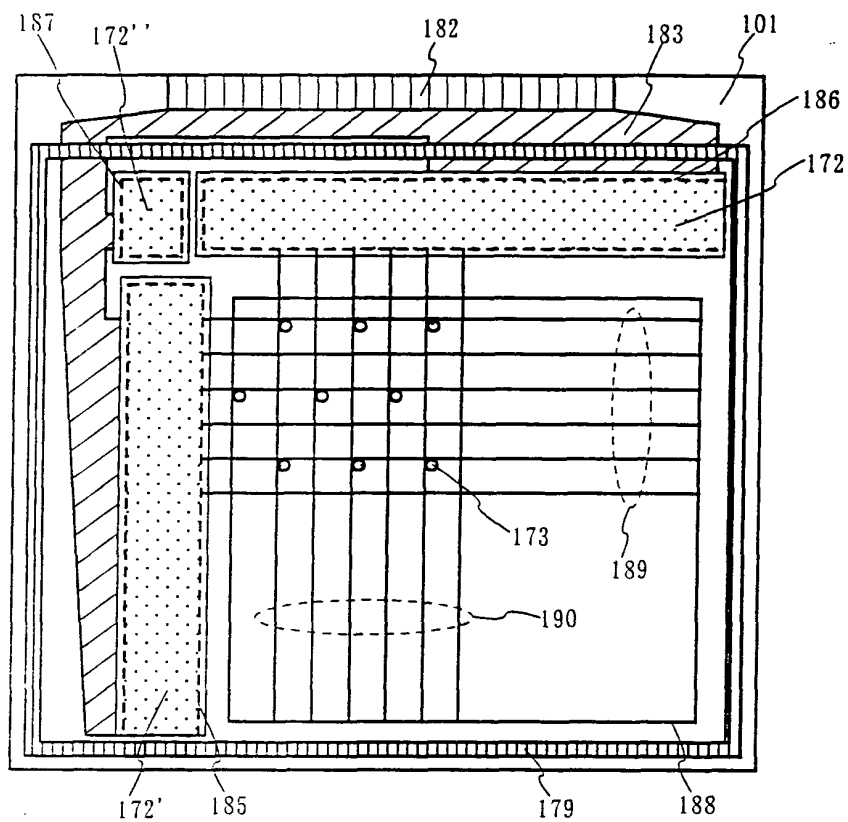


Fig. 14

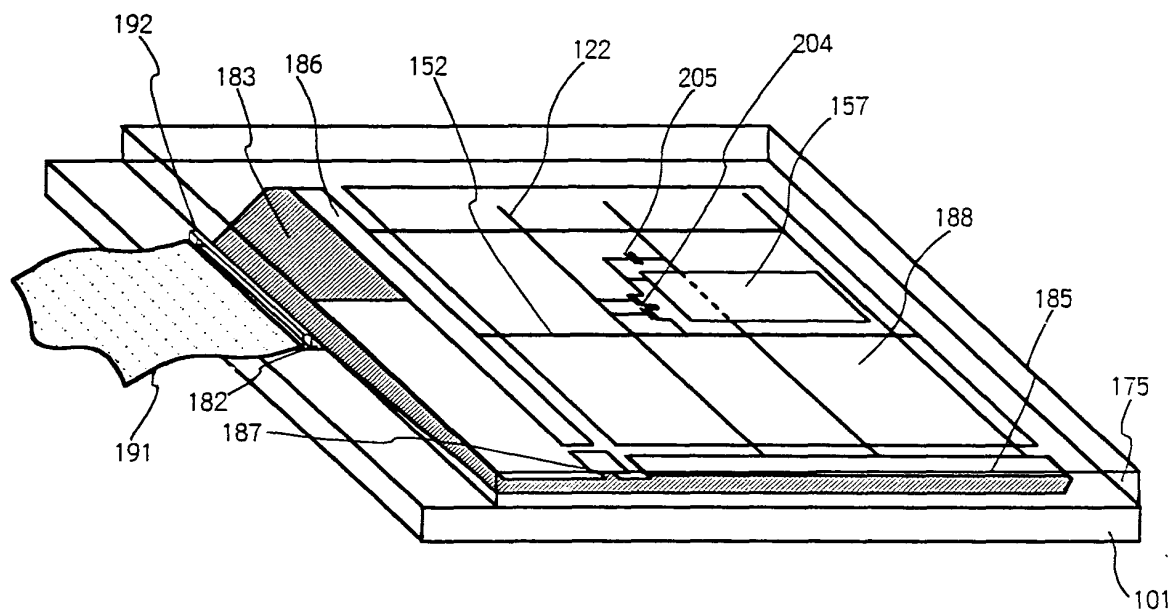
[illegible]

Fig. 15

005040-2FEU900

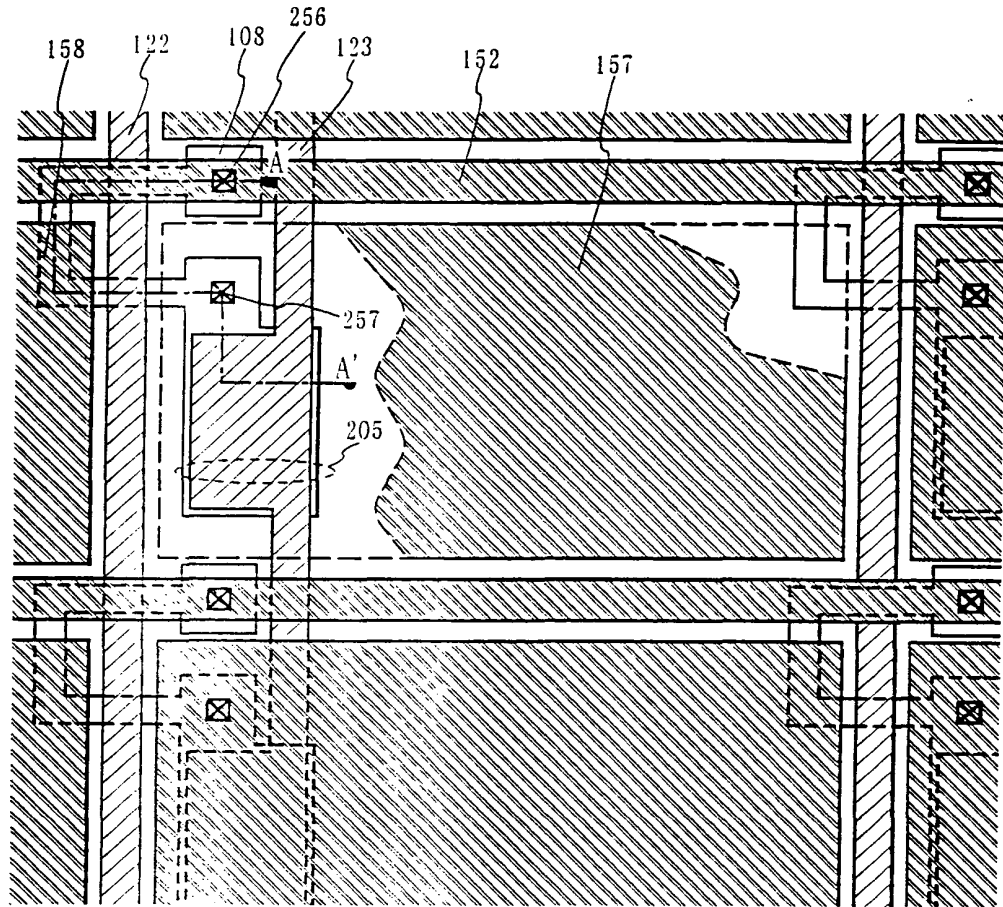


Fig. 16

The diagram shows two main functional blocks:

- (A) SCANNING SIGNAL DRIVING CIRCUIT**: This block contains four sub-circuits:
 - SHIFT REGISTER CIRCUIT (labeled 501b)
 - LEVEL SHIFTER CIRCUIT (labeled 502b)
 - CLOCK BUFFER CIRCUIT (labeled 503b)
 - PIXEL UNIT (labeled 504)
- PIXEL UNIT**: A large rectangular block labeled "188: PIXEL UNIT". It receives input from the scanning signal driving circuit.

Connections are indicated by arrows pointing from the output of each sub-circuit in block (A) towards the input of the Pixel Unit.

Fig. 17

Fig. 18

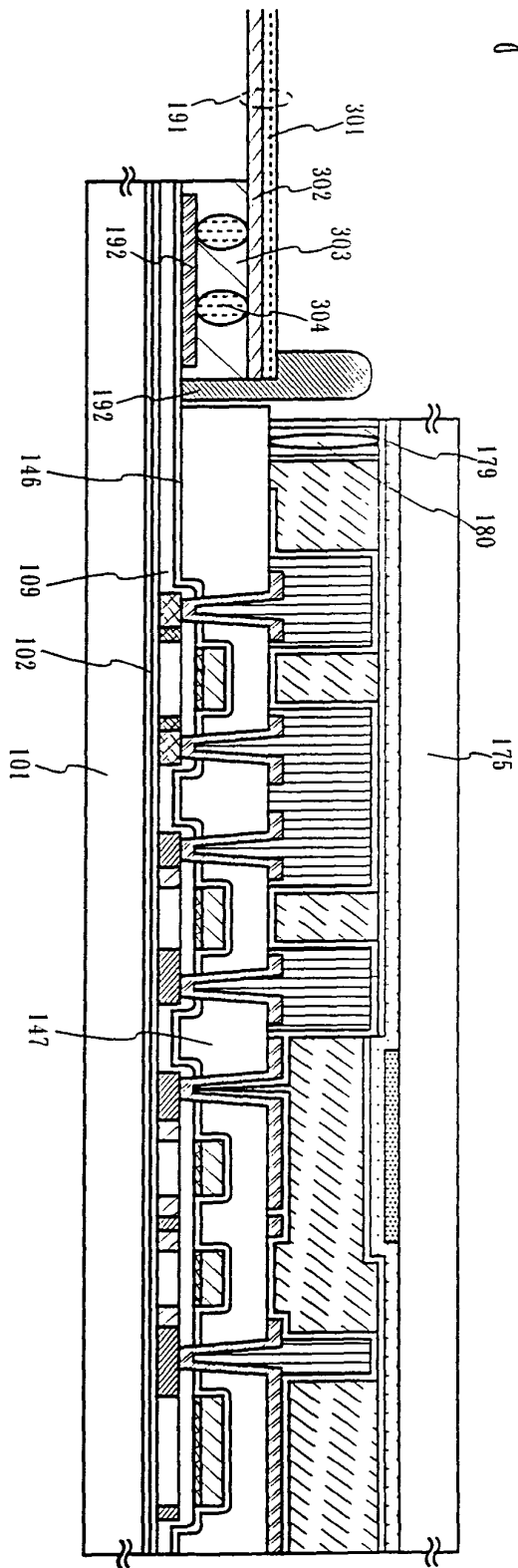


Fig. 19

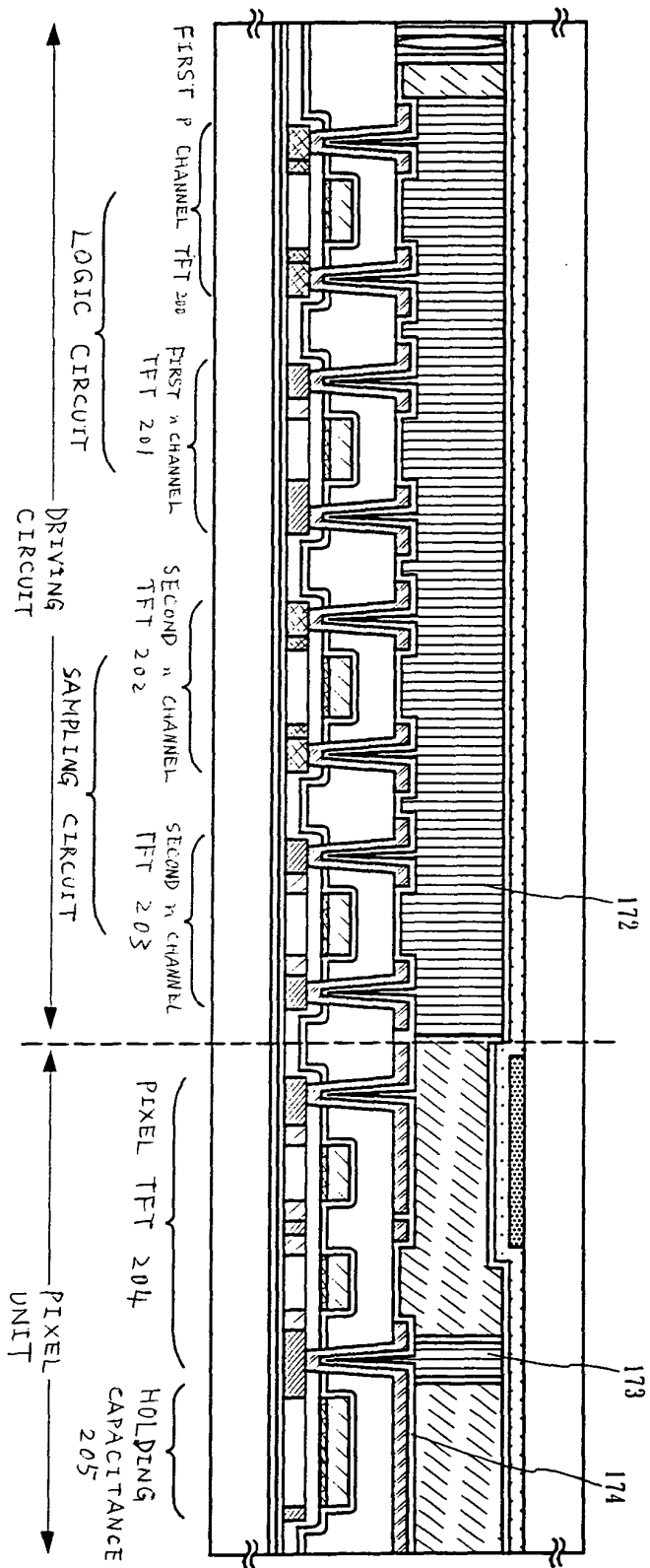
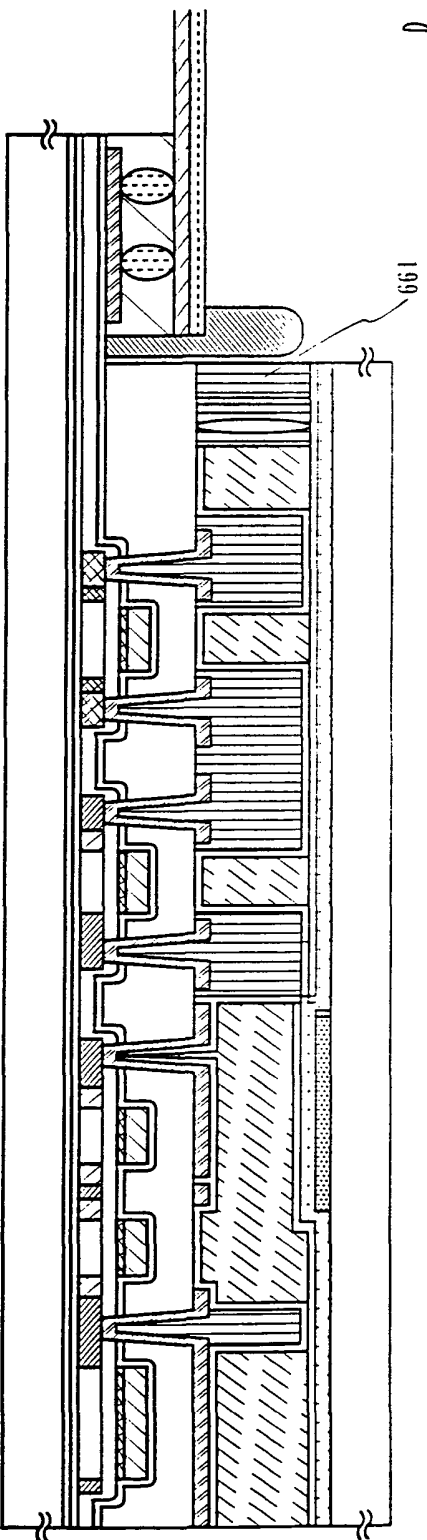
[illegible]

Fig. 20

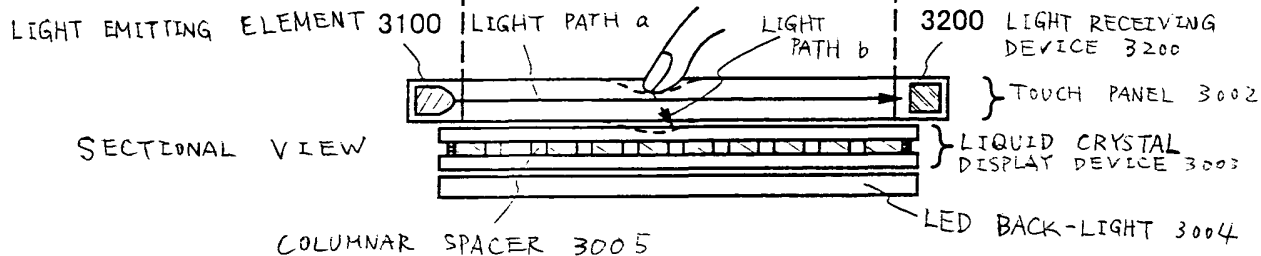


DISPLAY DEVICE 2205
TOUCH PANEL 3002
LCD PANEL 3003
LED BACK-LIGHT 3004

Fig. 21(A)

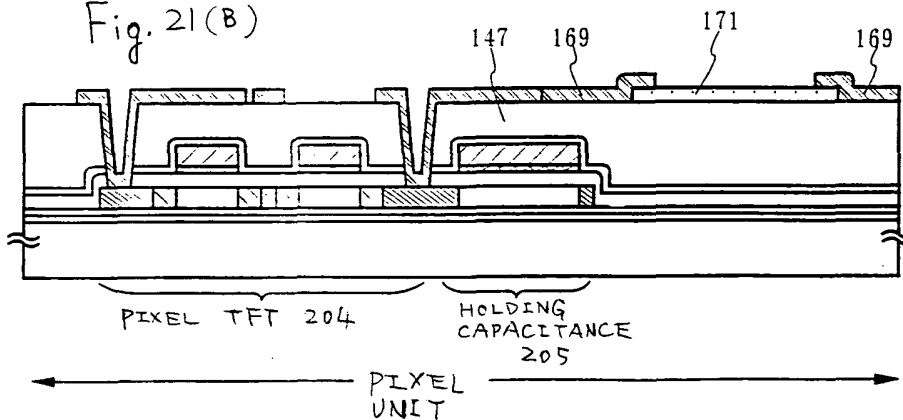
2202

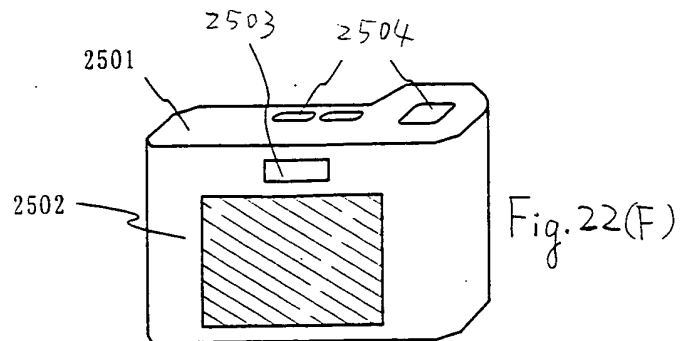
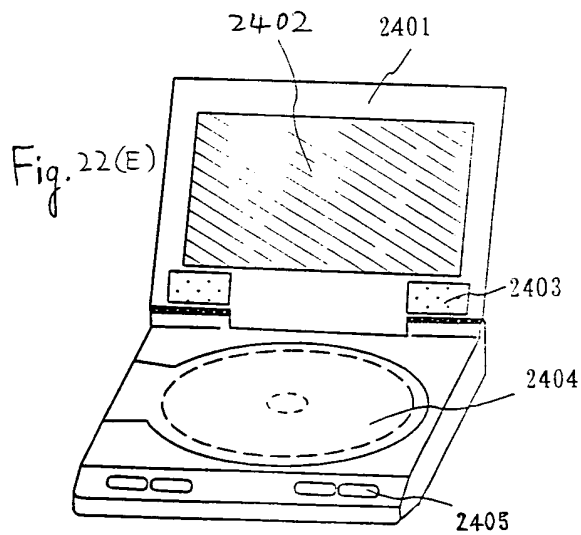
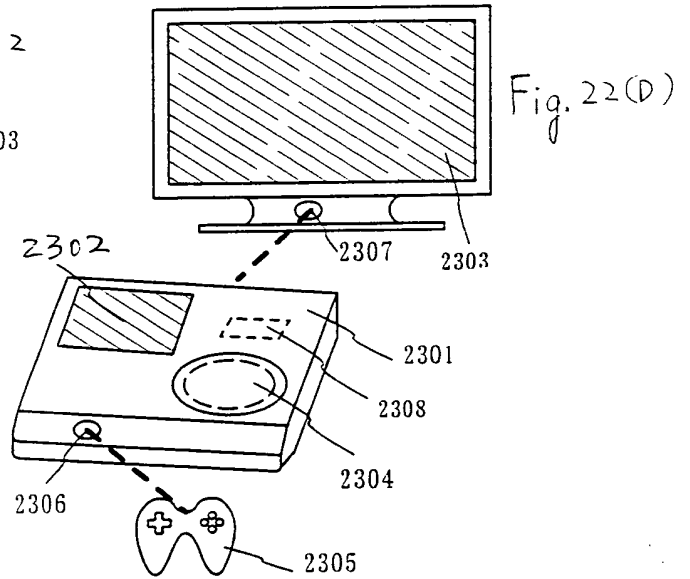
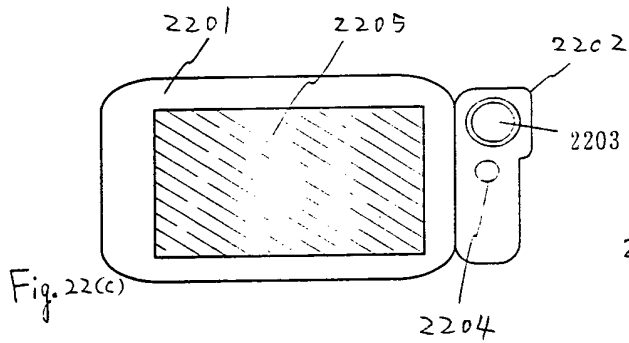
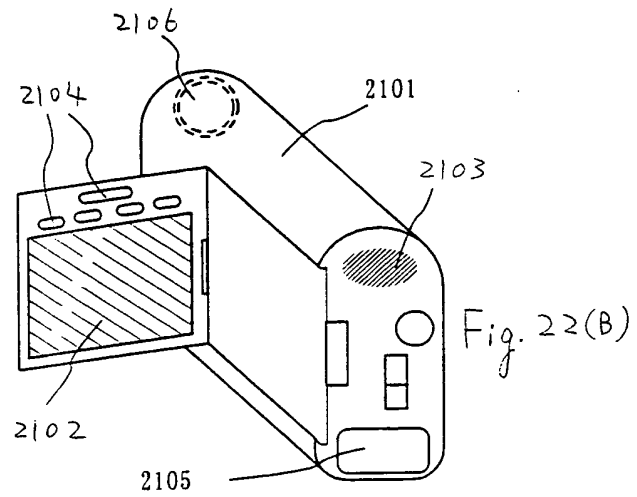
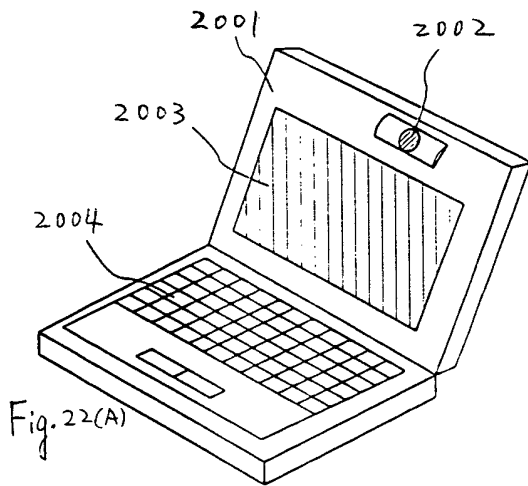
APPEARANCE VIEW



APPEARANCE VIEW AND SECTIONAL VIEW OF PORTABLE INFORMATION
TERMINAL (OPTICAL TOUCH PANEL)

Fig. 21(B)





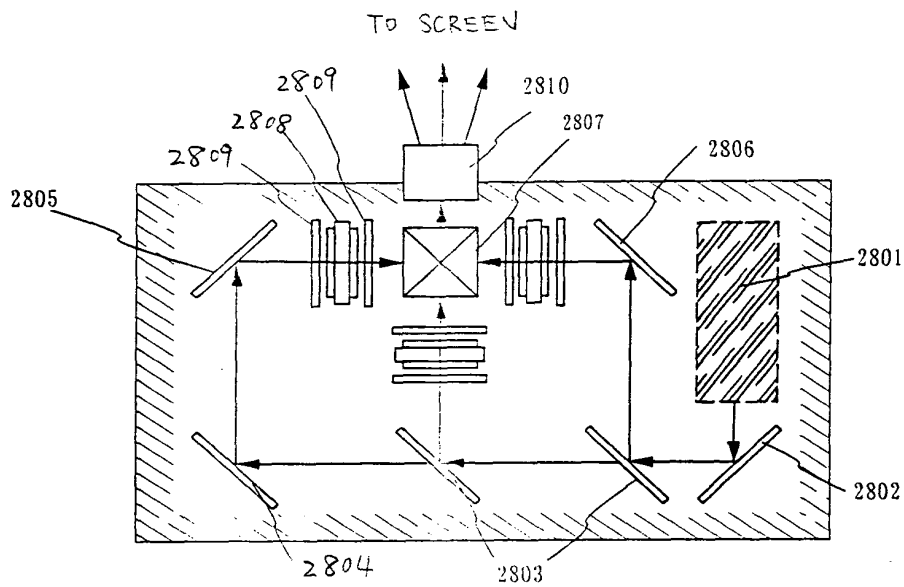
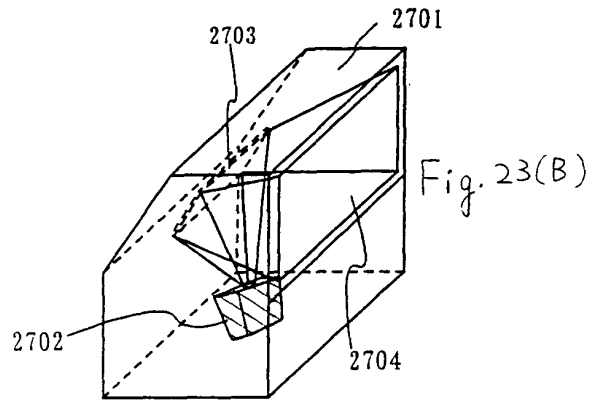
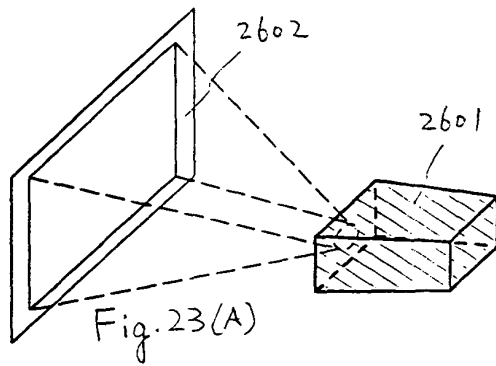


Fig. 23(C) LIGHT SOURCE OPTICAL SYSTEM AND DISPLAY DEVICE (THREE PLATE TYPE)

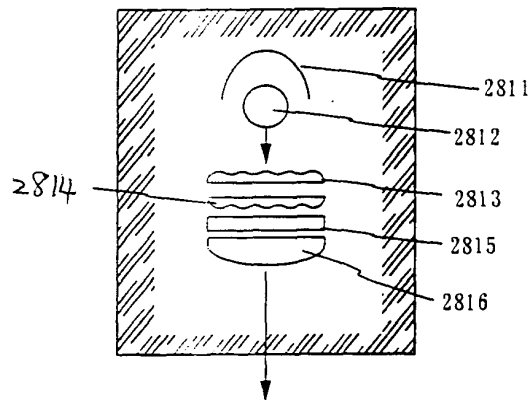


Fig. 23(D)
LIGHT SOURCE
OPTICAL SYSTEM